		Ref.		
* COMBINED I	DECLARATION A	ND POWER OF A	TTORNEY	
As a below named inven	tor, I hereby declare that			
My residence, post office	address and citizenship ar	e as stated below next to m	y name.	
I believe I am the original and joint inventor (if plura a patent is sought on the	d names are listed below) of	only one name is listed belov the subject matter which is cl	v) or an original, first aimed and for which	
LATCH CIRCUIT FOR	R LATCHING A PAIR OF CO	MPLEMENTARY DATA SIGNAL	S	
the specification of which	n: <i>(check one)</i>			
	REGULAR OR DESIG	N APPLICATION		
[] is atta	ched hereto.			
[] was fi	led on and was ame	as application Serial No	o. -	
(II app			•	
() was d	PCT FILED APPLICATION ENTERING NATIONAL STAGE was described and claimed in International application No.			
was described and claimed in inter- filed on filed on		(if any).		
as amended by any amendme	wed and understand the contents nt referred to above.	of the above-identified specificati		
	PRIORITY	CLAIM		
listed below and have also ide	benefits under 35 USC 119 of a intified below any foreign applica on which priority is claimed.	ny foreign application(s) for paten ation for patent or inventor's certif	t or inventor's certificate icate having a filing date	
·. ·	PRIOR FOREIGN A	PPLICATION(S)		
Country	Application Number	Date of Filing (day, month, year)	Priority Claimed	
Japan	2002-242403	22/08/2002	Yes	
·		<u> </u>		
matter of each of the claims of by the first paragraph of 35 Ut defined in Title 37 Code of Fed	der 35 USC 120 of any United Staths application is not disclosed in SC 112, I acknowledge the duty t	tates application(s) listed below as in the prior United States application to disclose information which is marme available between the filing da ation:	n in the manner provided aterial to patentability as	
and the management of the little				

(Status-patented, pending, abandoned)

(Application Serial No.)

....

POWER OF ATTORNEY

As a named inventor, I hereby appoint the following attorney(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Robert J. PATCH, Reg. No. 17,355, Andrew J. PATCH, Reg. No. 32,925, Robert F. HARGEST, Reg. No. 25,590, Benoît CASTEL, Reg. No. 35,041, Eric JENSEN, Reg. No. 37,855, and Thomas W. PERKINS, Reg. No. 33,027, c/o YOUNG & THOMPSON, Second Floor, 745 South 23rd Street, Arlington, Virginia 22202.

Address all telephone calls to Young & Thompson at 703/521-2297.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor: Yasushi AOKI (given name, family name)			
Inventor's signature Yasushi aoki	Date August 20, 2003		
Residence: Kanagawa, Japan	Citizenship: Japanese		
Post Office Address: c/o NEC Electronics Corporation, 1753 Shimonumabe, Nakahara-ku, Kawasaki, Kanagawa 211-8668, Japan			
Full name of second joint inventor, if any: (given name, family name)			
Inventor's signature	Date		
Residence:	Citizenship:		
Post Office Address:			
	•		
Full name of third joint inventor, if any: (given name, family name)			
Inventor's signature	Date		
Residence:	Citizenship:		
Pact Office Address:			